

FIG. 1

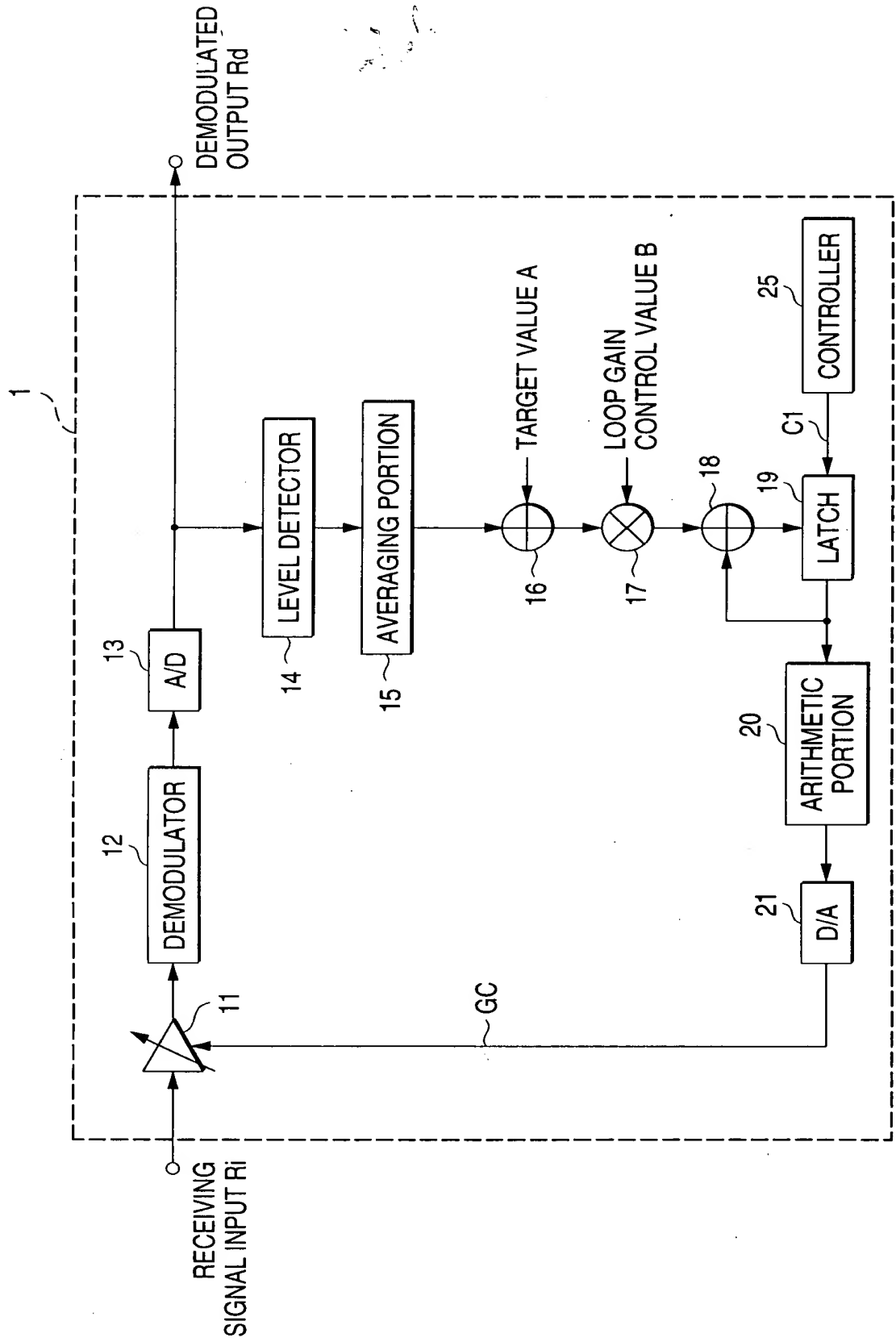


FIG. 2A

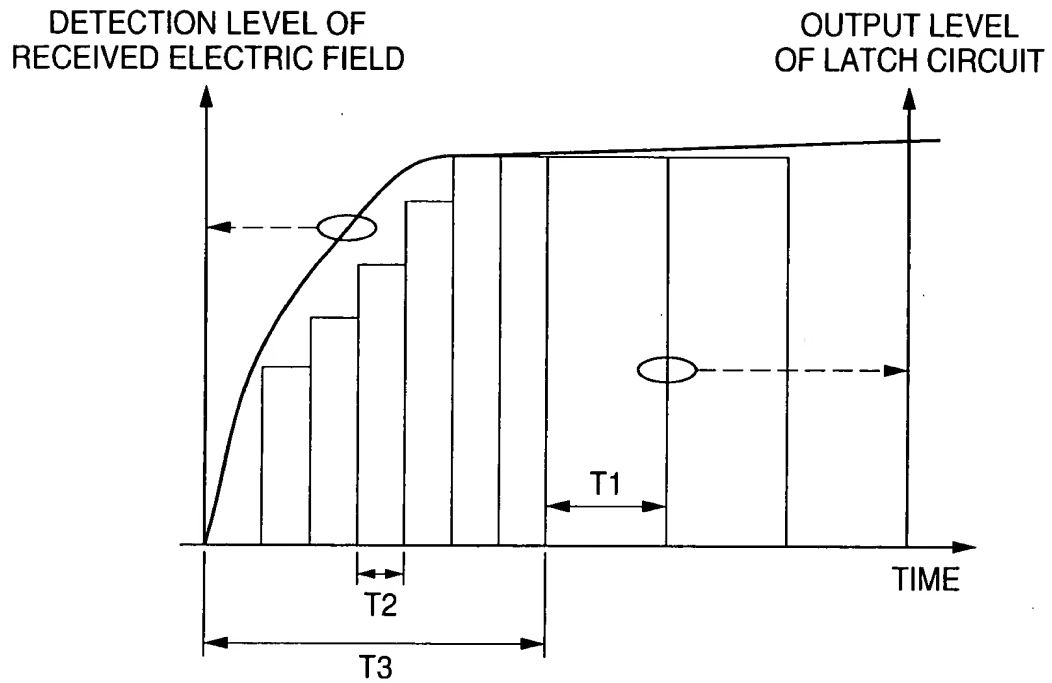


FIG. 2B

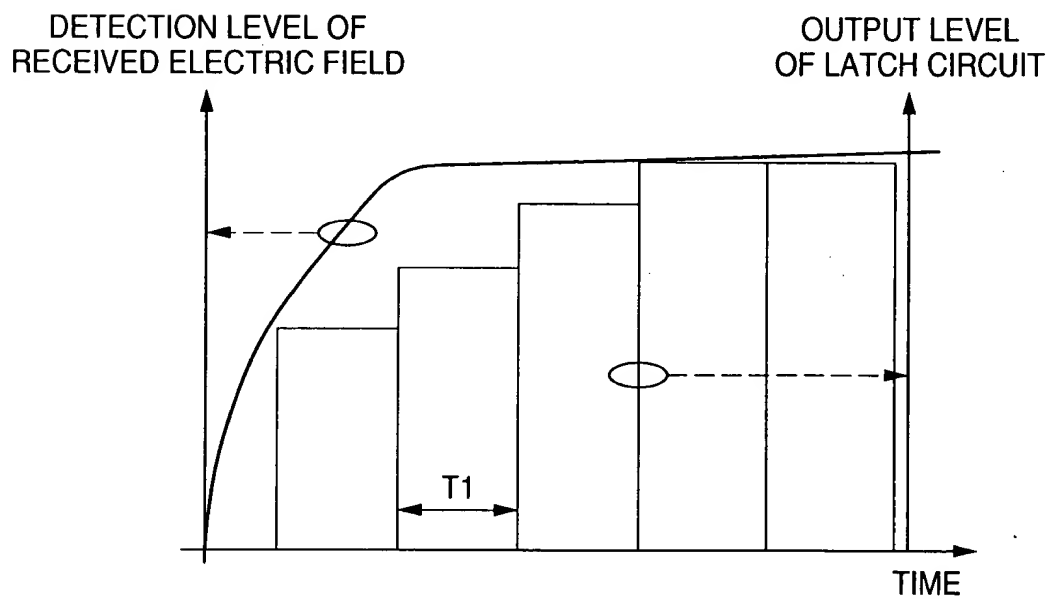


FIG. 3

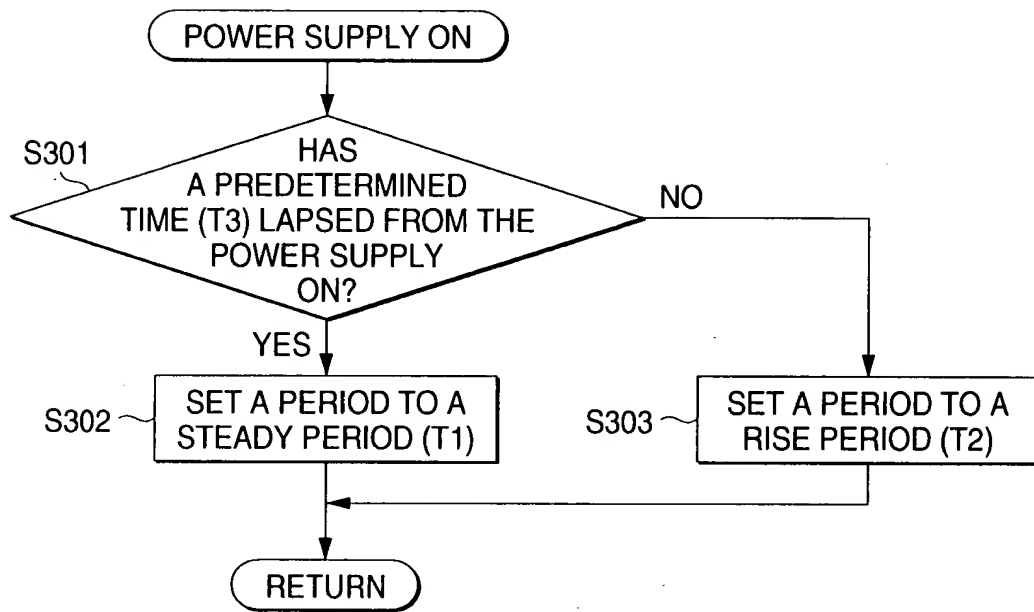


FIG. 4

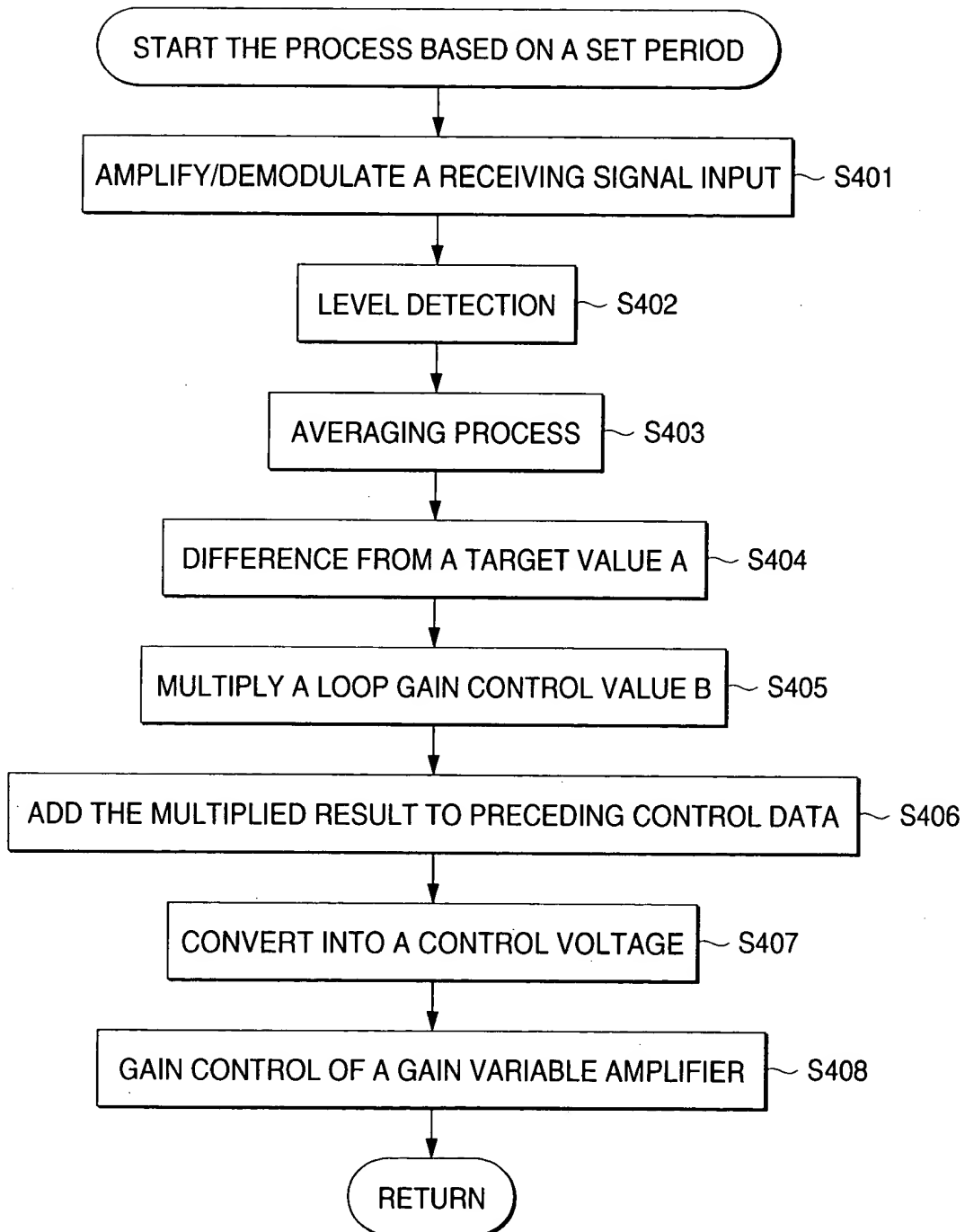


FIG. 5A

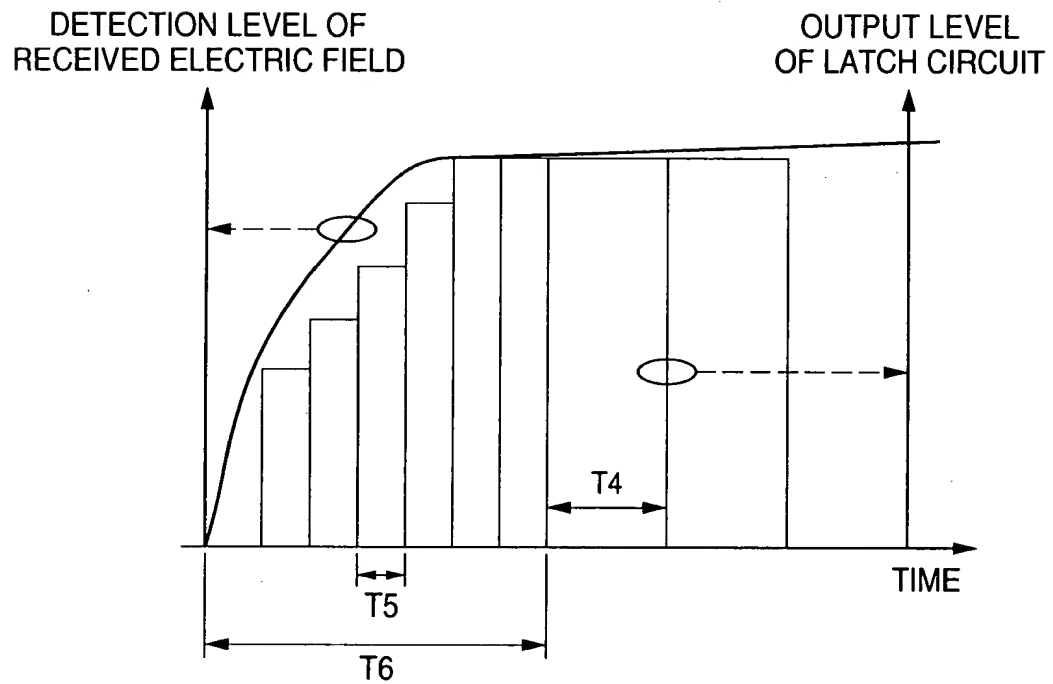


FIG. 5B

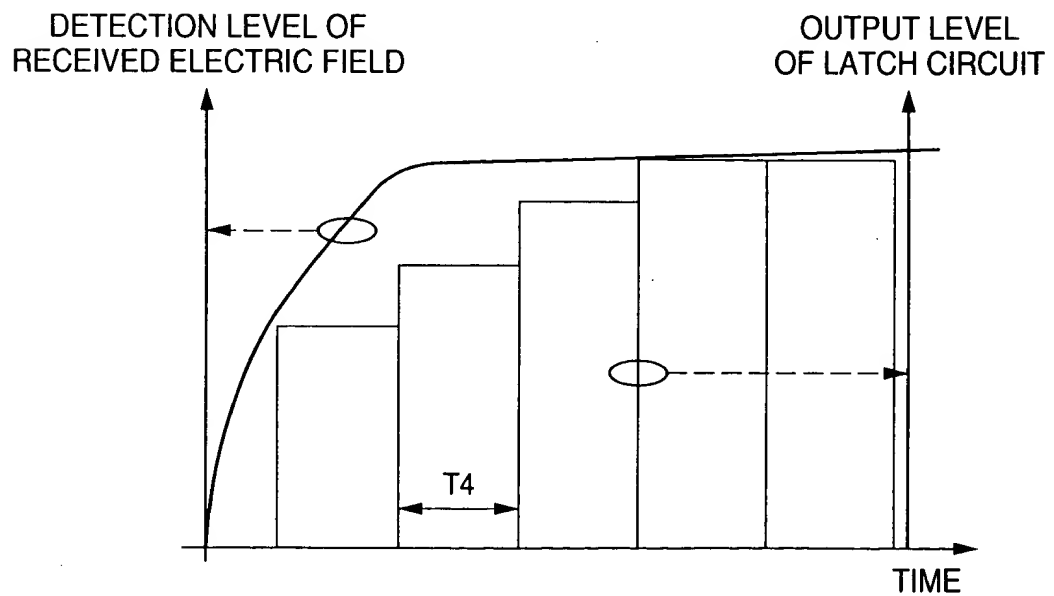


FIG. 6

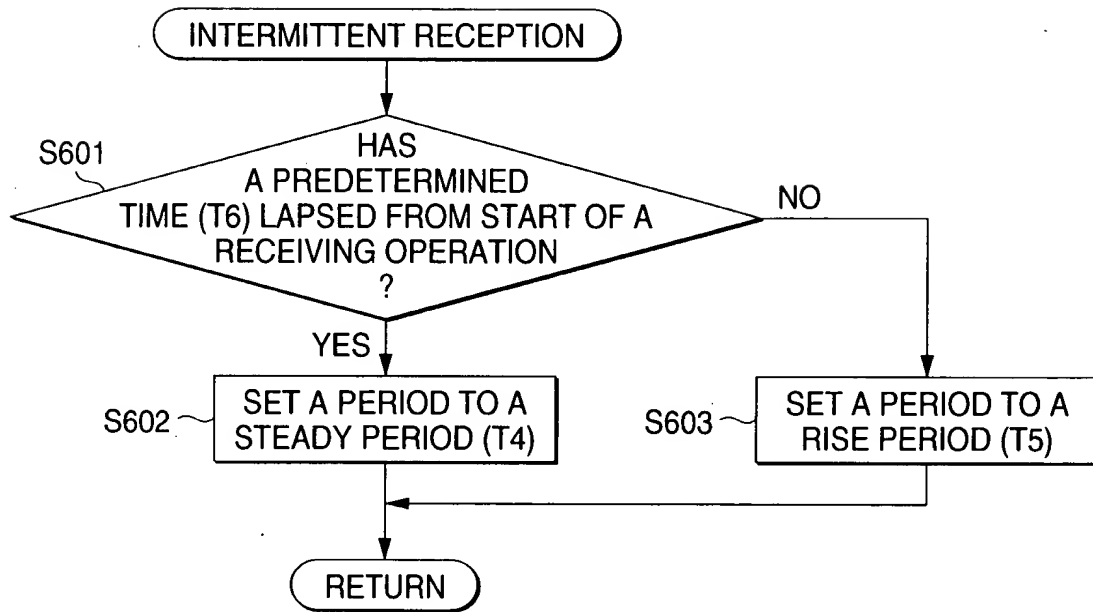


FIG. 7

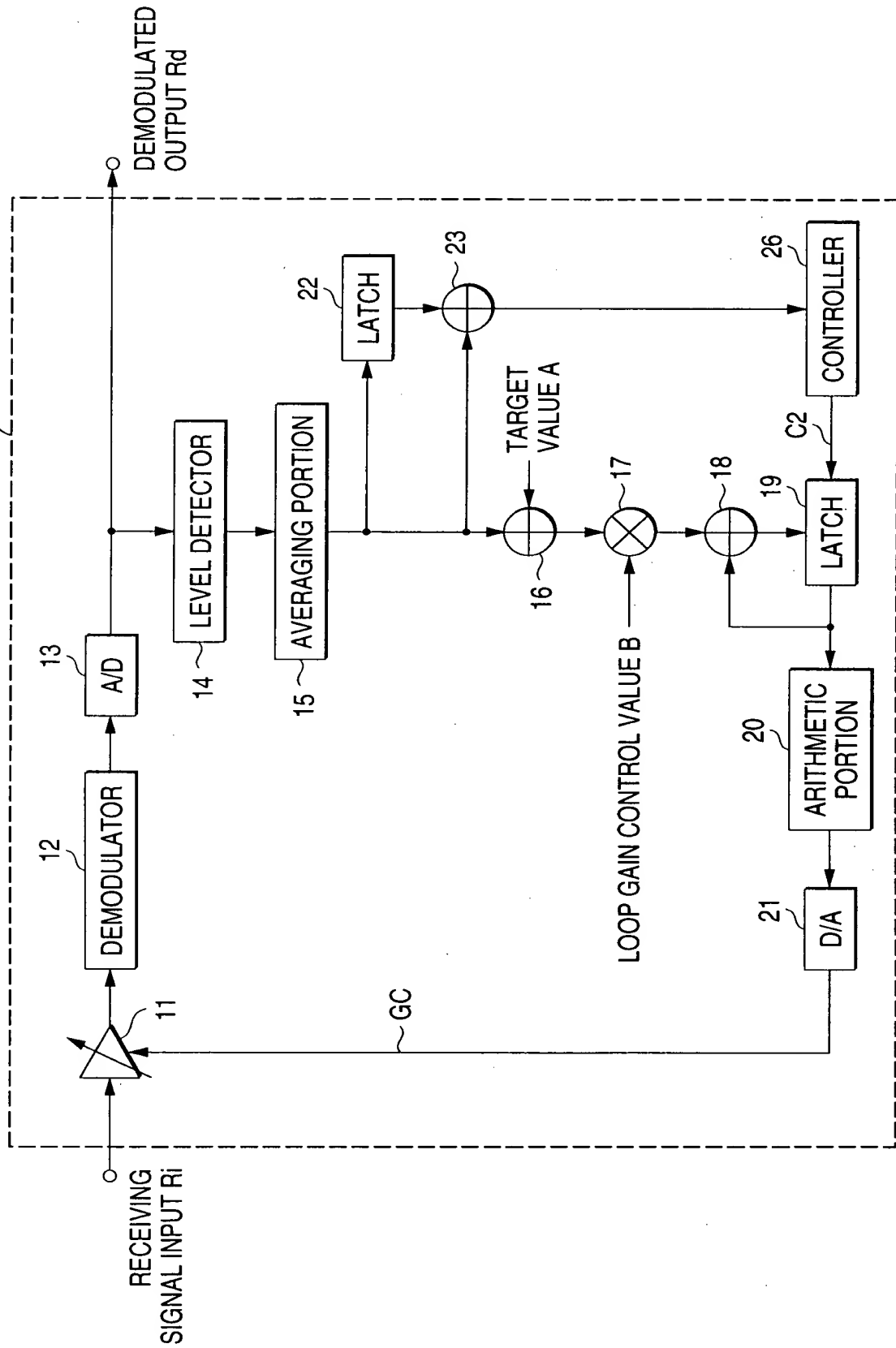


FIG. 8A

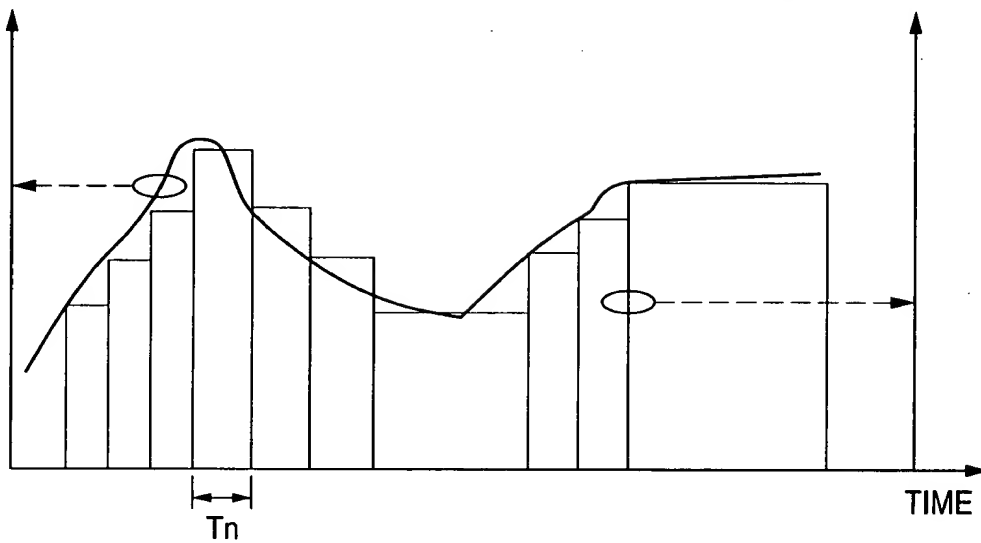
DETECTION LEVEL OF
RECEIVED ELECTRIC FIELDOUTPUT LEVEL
OF LATCH CIRCUIT

FIG. 8B

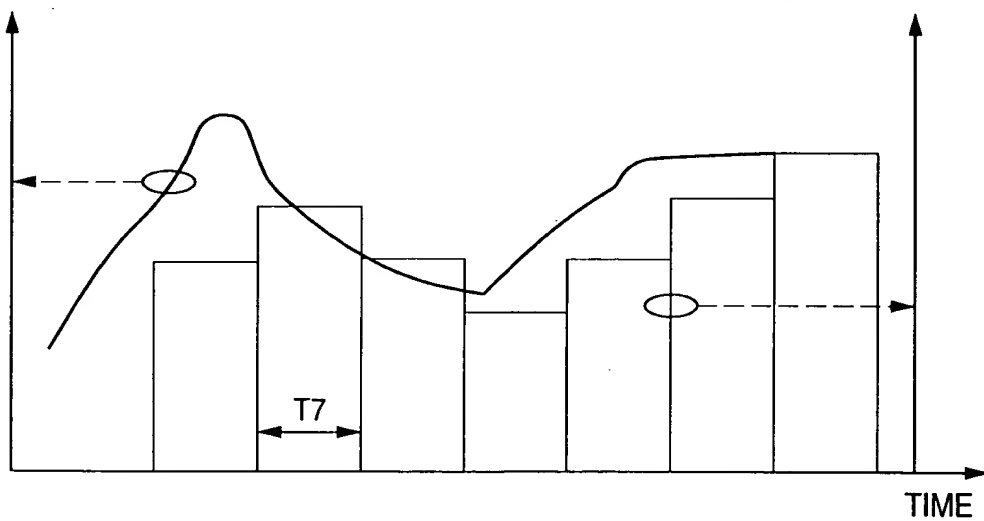
DETECTION LEVEL OF
RECEIVED ELECTRIC FIELDOUTPUT LEVEL
OF LATCH CIRCUIT

FIG. 9

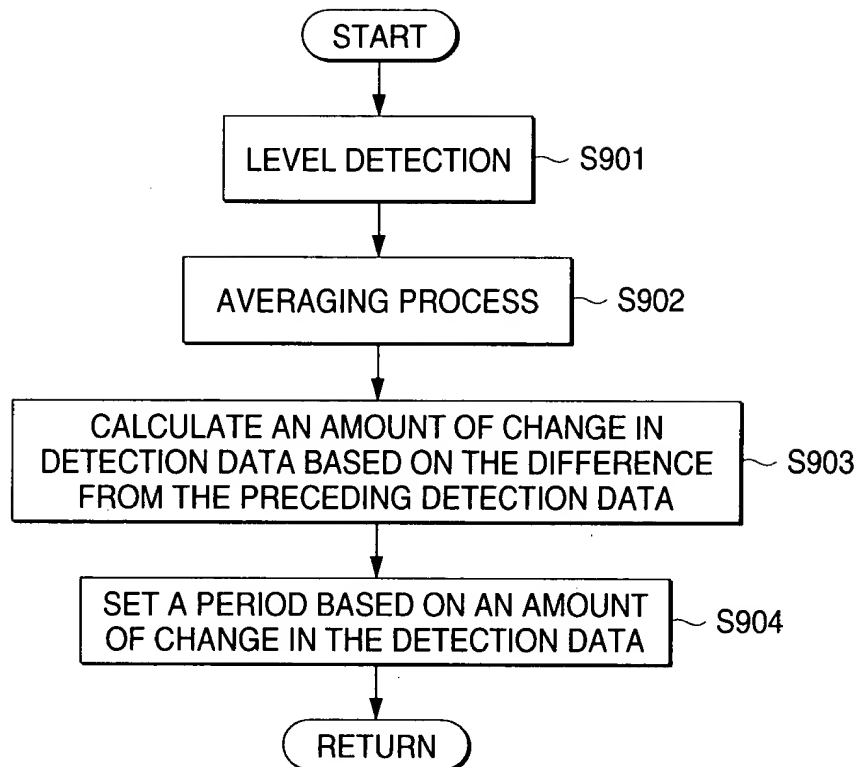


FIG. 10

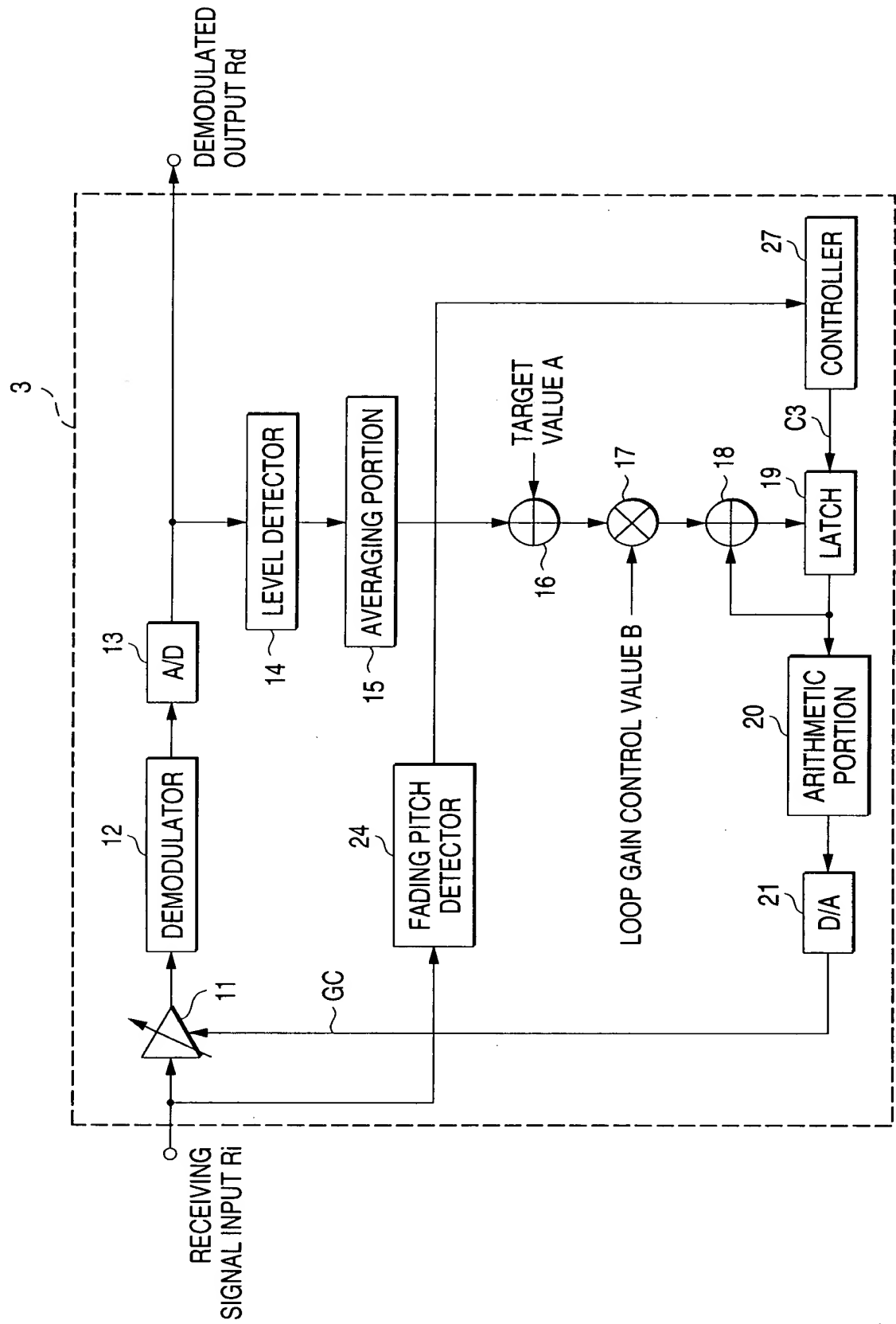


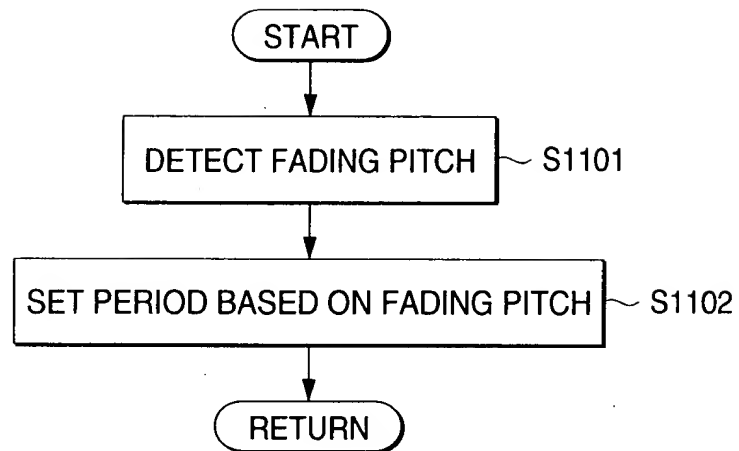
FIG. 11

FIG. 12

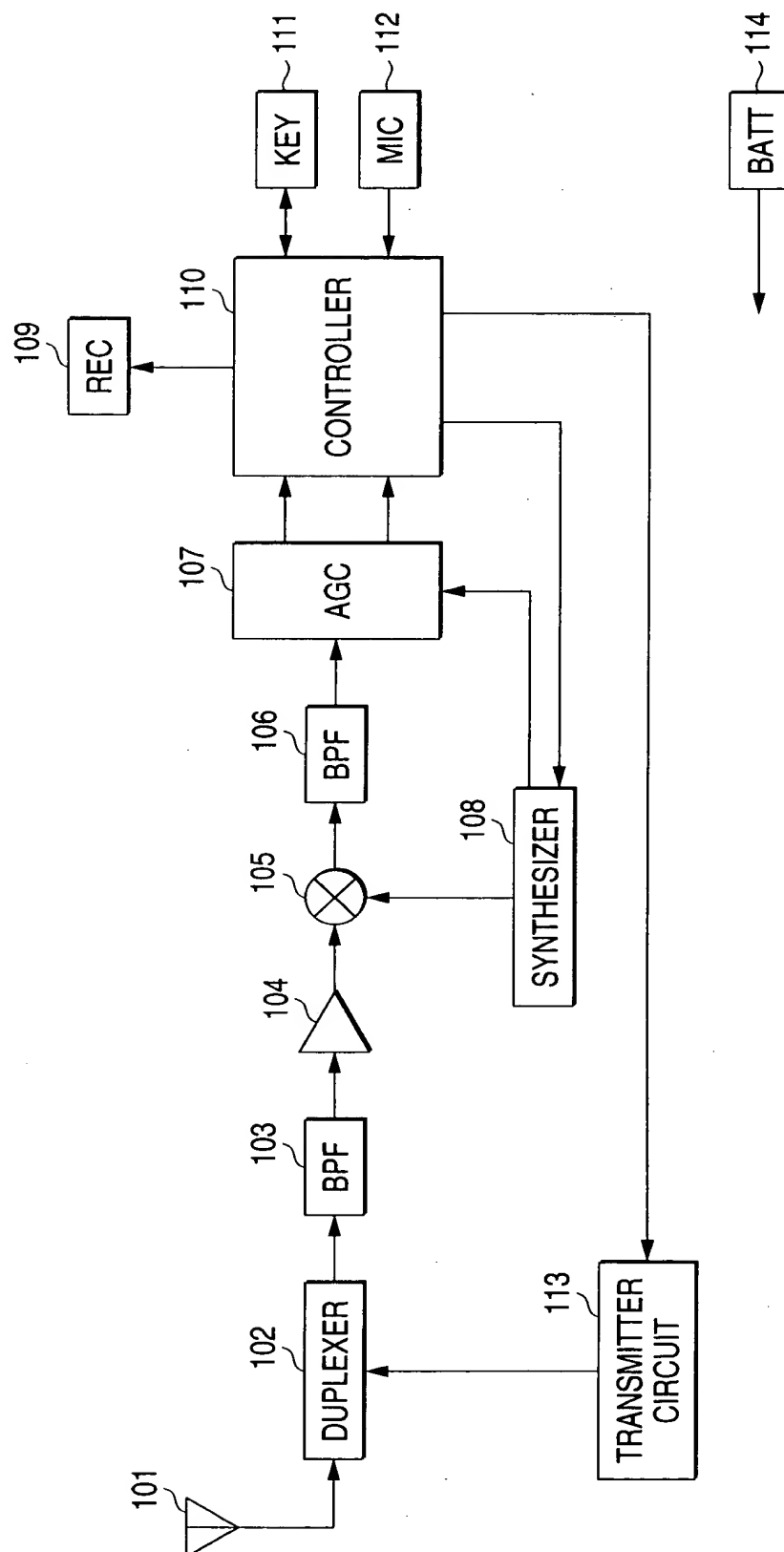


FIG. 13

